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# **GemStone/S 64 Bit<sup>TM</sup>**

## **Release Notes**

**Version 3.5.6.4**

June 2022



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## PATENTS

GemStone software is or has been covered by U.S. Patent Number 6,256,637 "Transactional virtual machine architecture" (1998-2018), Patent Number 6,360,219 "Object queues with concurrent updating" (1998-2018), Patent Number 6,567,905 "Generational garbage collector with persistent object cache" (2001-2021), and Patent Number 6,681,226 "Selective pessimistic locking for a concurrently updateable database" (2001-2021).

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# Release Notes for 3.5.6.4

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GemStone/S 64 Bit™ 3.5.6.4 is a limited-distribution special release version of the GemStone/S 64 Bit object server.

These Release Notes include changes between the previous version of GemStone/S 64 Bit, v3.5.6.3, and v3.5.6.4.

## Supported Platforms

GemStone/S 64 Bit version 3.5.6.4 is supported on Linux only, and with clients running on Windows and Linux.

This release is not supported on 32-bit clients. The distribution does not include the `$GEMSTONE/lib32/` and `$GEMSTONE/bin32/` directories.

## Upgrade

GemStone/S 64 Bit v3.5.6.4 contains no image-level changes from v3.5.6.3, 3.5.6.2, or 3.5.6.1. While running **upgradeImage** is generally recommended, it is not required when upgrading from 3.5.6, 3.5.6.1, or 3.5.6.2. For these upgrades, you may restart GemStone with the new executables without running **upgradeImage**.

## Improved printing of GCI trace records on protocol error

The previous 3.5.6.x versions included GCI trace record printing to aid in analysis of GCI protocol errors. Recent GCI call history is automatically printed to the gem log when a GCI protocol error occurs. GCI call trace records are maintained in a wraparound buffer, and the most recent 100 records are printed to the log.

This release include larger history buffers to allow printing of a longer call history, and printing of additional socket-level details.

Due to the larger history buffer, the memory footprint of libgcirpc and gem each increases by 16MB.

There have also been some adjustments to socket reading code.